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BUR920010040US2  
Serial No. 10/695,335

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jeffrey P. Gambino, et al.

Examiner: Shrinivas H. Rao

Serial No: 10/695,335

Art Unit: 2814

Filed: October 28, 2003

Docket: BUR920010040US2

For: FIN FIELD EFFECT TRANSISTOR  
WITH SELF-ALIGNED GATE

Date: January 26, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

Sir:

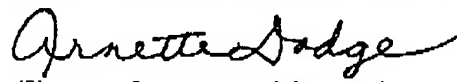
This is in response to the Notice of Non-Compliant Amendment of Dec. 16, 2004.

This response includes a re-submission (in its entirety) of the "Amendments to the Claims" section of Applicants' Amendment and Response of December 1, 2004.

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CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8

I certify that this correspondence of 5 pages is being transmitted by facsimile to the United States Patent and Trademark Office to: Examiner Shrinivas H. Rao, Art Unit 2814 at facsimile number 703 872-9306 on January 26, 2005 by Arnette Dodge

  
(Signature of person transmitting paper)  
ARNETTE DODGE